

Total number of chips	Chip ID	Threshold ( $V_{th0}$ )						Noise					
		1.95V		2.12V		2.20V		1.95V		2.12V		2.20V	
		$\mu [e^-]$	$\sigma [e^-]$	$\mu [e^-]$	$\sigma [e^-]$	$\mu [e^-]$	$\sigma [e^-]$	$\mu [e^-]$	$\sigma [e^-]$	$\mu [e^-]$	$\sigma [e^-]$	$\mu [e^-]$	$\sigma [e^-]$
5		1.95V		2.12V		2.20V		1.95V		2.12V		2.20V	
	0	6504	278.83	3189.19	269.47	1672.85	257.23	41	6.13	41.77	7.97	38.84	8.76
	1	6431	216.55	3212	200.16	1755.3	185	37.7	6.5	38.56	6.78	35.83	5.38
	2	6298.87	217.61	3055.8	209.55	1611.83	189.4	43.7	7.51	43.36	7.54	38.53	7.13
	3	6524.82	240.9	3292.53	231.96	1795.94	211.83	43.8	7.5	44.53	7.72	39.38	6.83
	4	6339.5	277.67	3097.19	254	1596.64	217	43.93	6.9	43	7	42.88	8.56
4		1.95V		2.12V		2.20V		1.95V		2.12V		2.20V	
	0	6285.11	240.2	3109.94	228.7	1647.53	225.82	41.81	8.77	43.7	8.68	40.07	9.65
	1	6197.26	207.42	3109.89	205.4	1651.82	174.13	39.88	9.02	39.94	7.72	36.09	7.46
	2	6093.81	217.93	2964.34	215.82	1533.85	178.18	45.39	8.55	45.14	8.55	39.66	8.77
	3	6274.94	233.76	3167.14	221.65	1693	200.7	43.31	8.3	44.8	8.18	41.64	7.8
3		1.95V		2.12V		2.25V		1.95V		2.12V		2.25V	
	0	7050	132.38	4815	134.25	2201.25	180	35.72	7.82	41.69	9.86	46.7	14.12
	1	7106.25	105	4867.5	105	2332.5	134.25	34	8.18	38.8	13.57	43.74	13.08
	2	7143.75	124.13	4833.75	187.5	2296.12	132.38	39.84	11.97	44.13	11.25	49.56	11.26

\* Values in red represent the actual voltage levels in the  $V_{th0}$  input of the FPIX1 chips.