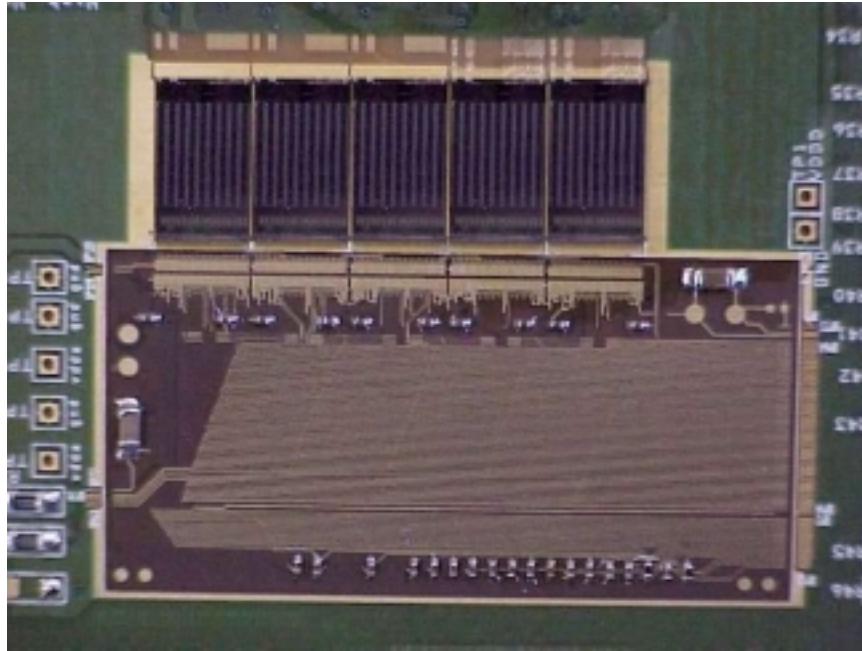
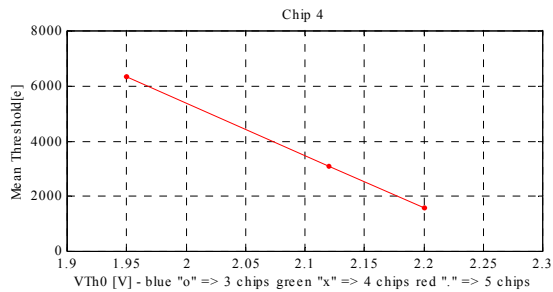
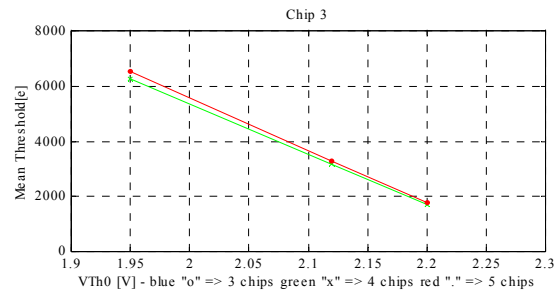
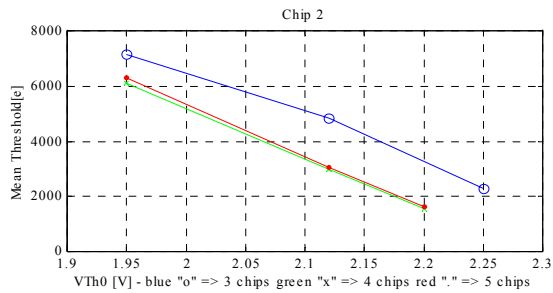
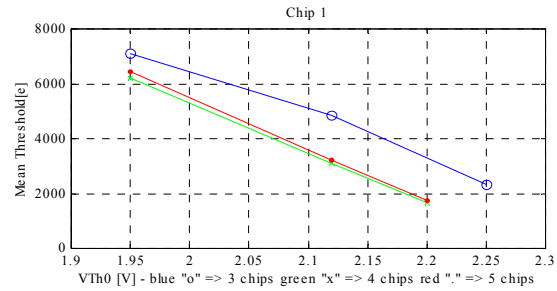
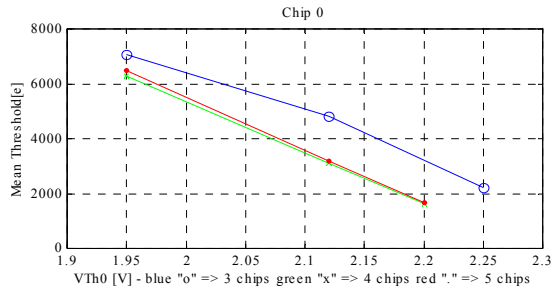


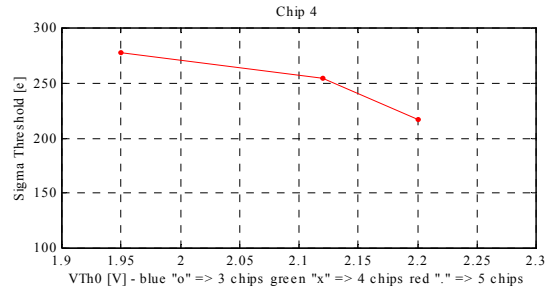
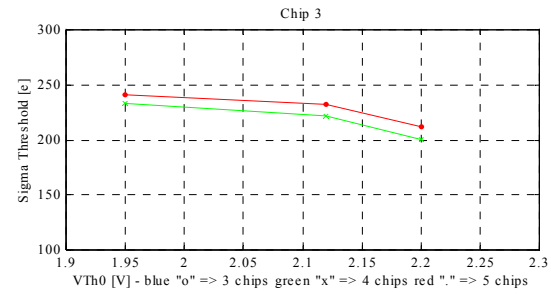
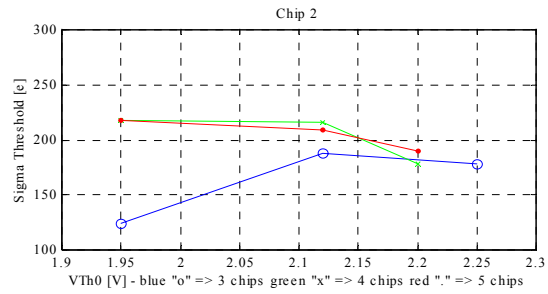
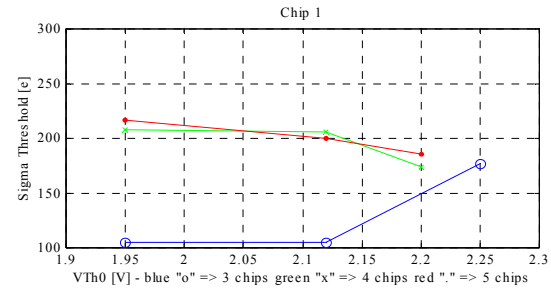
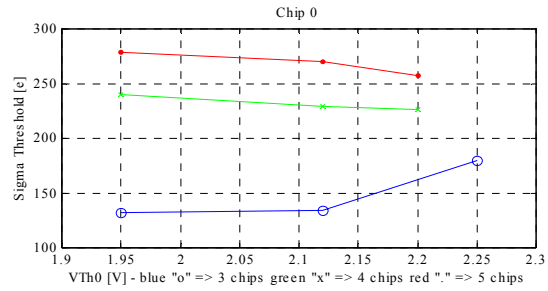
Final Results 5 Chips without Detector



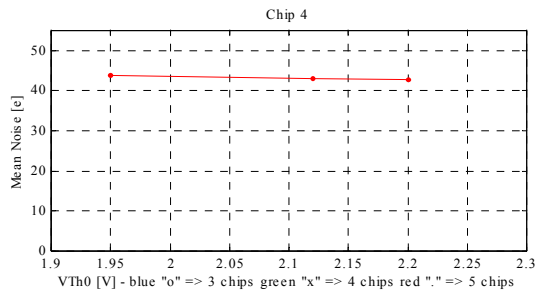
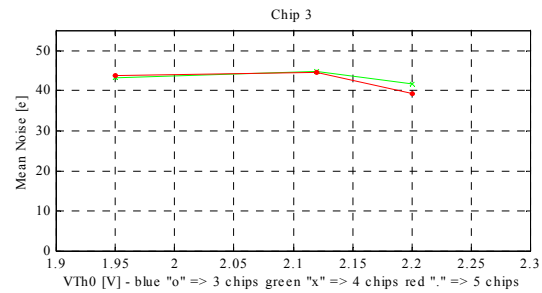
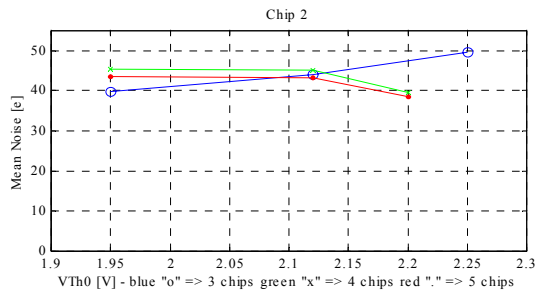
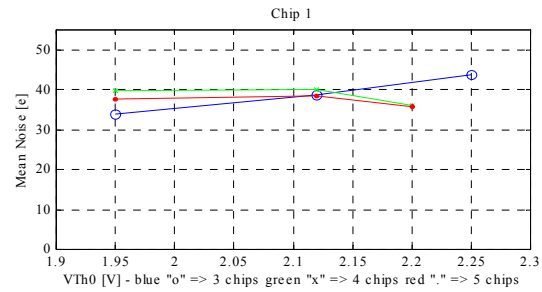
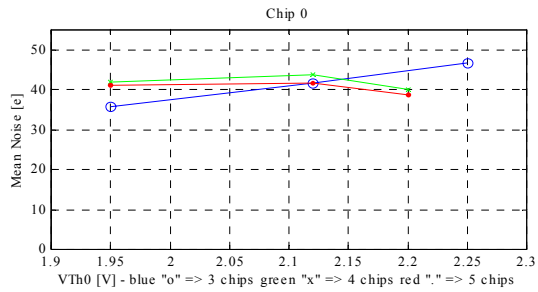
Mean Threshold Comparison



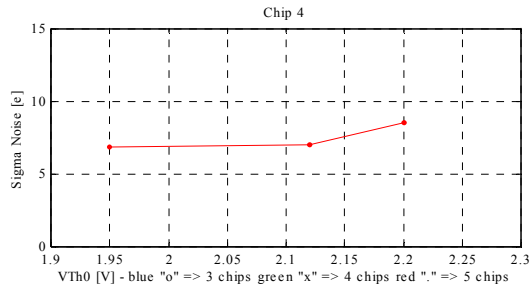
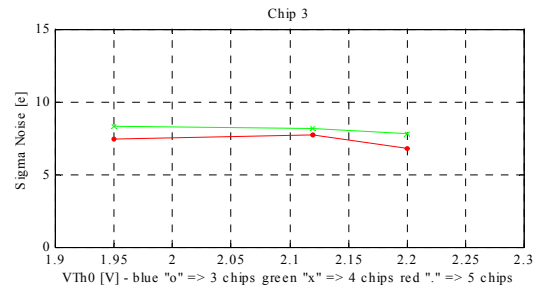
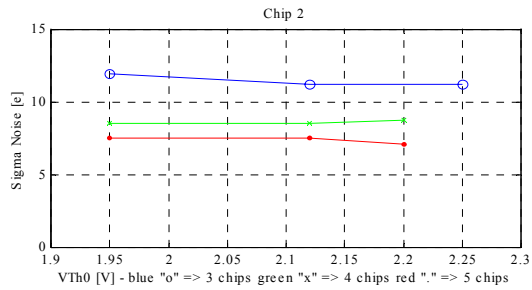
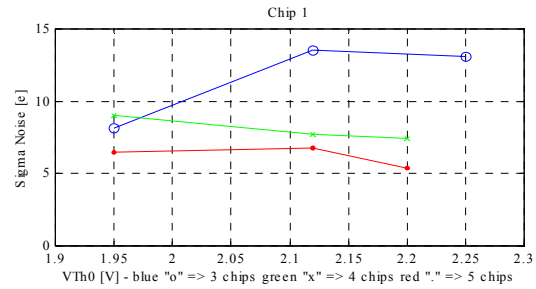
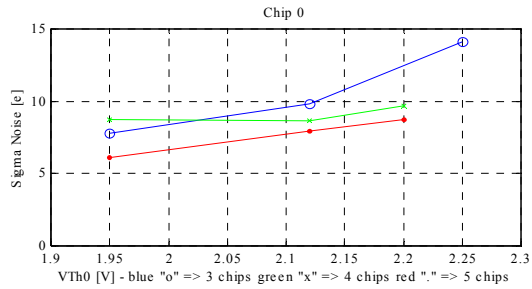
Sigma Threshold Comparison



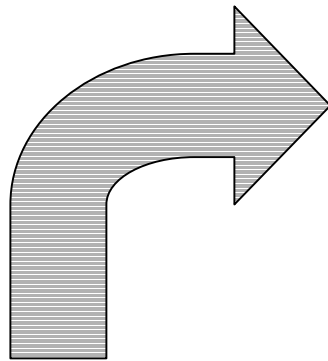
Mean Noise Comparison



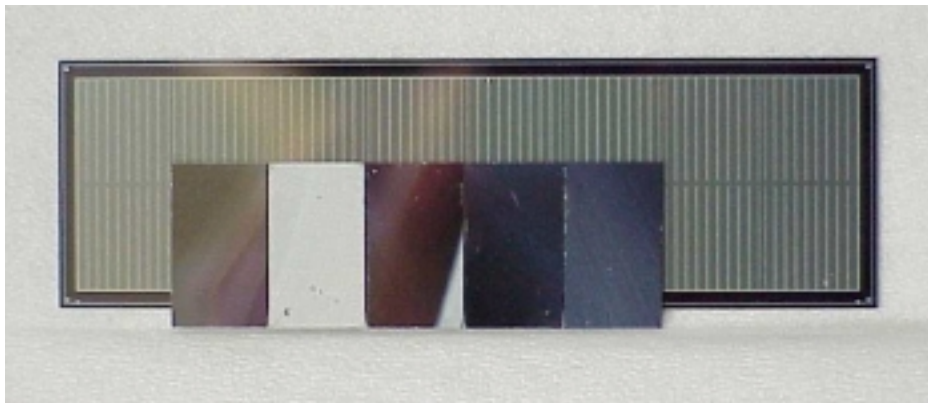
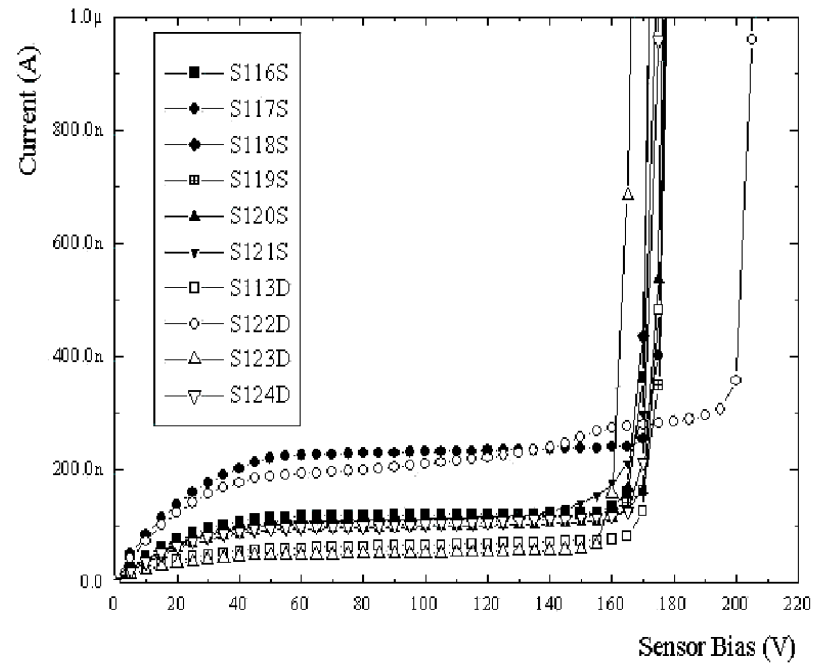
Sigma Noise Comparison



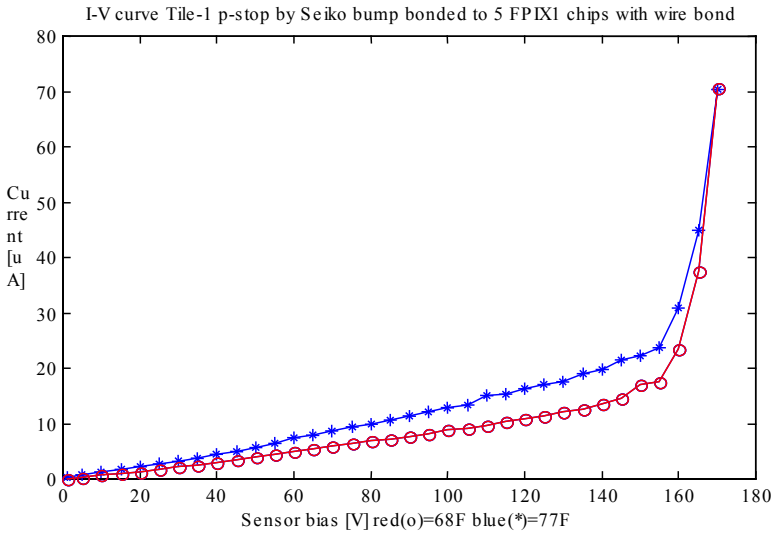
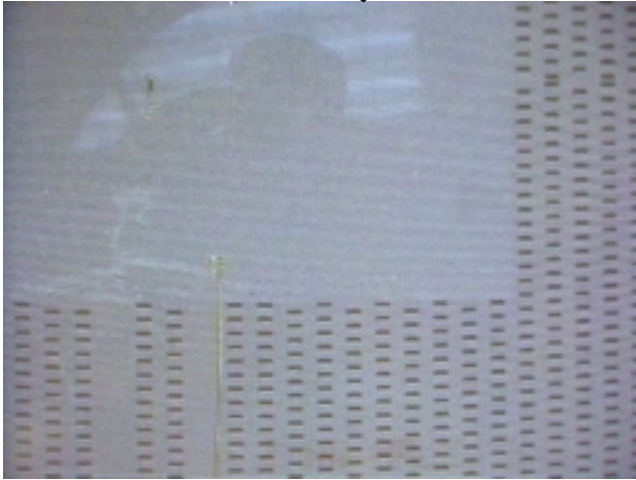
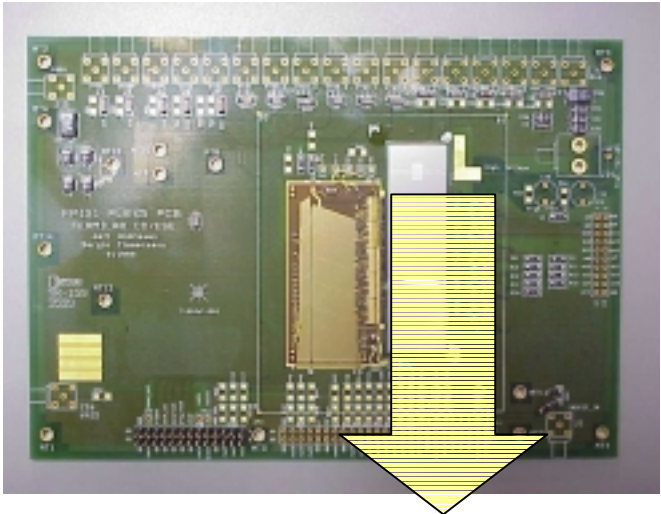
Atlas Tile 1 P-Stop
Seiko Detector bump
bonded to 5 FPIX1
chips



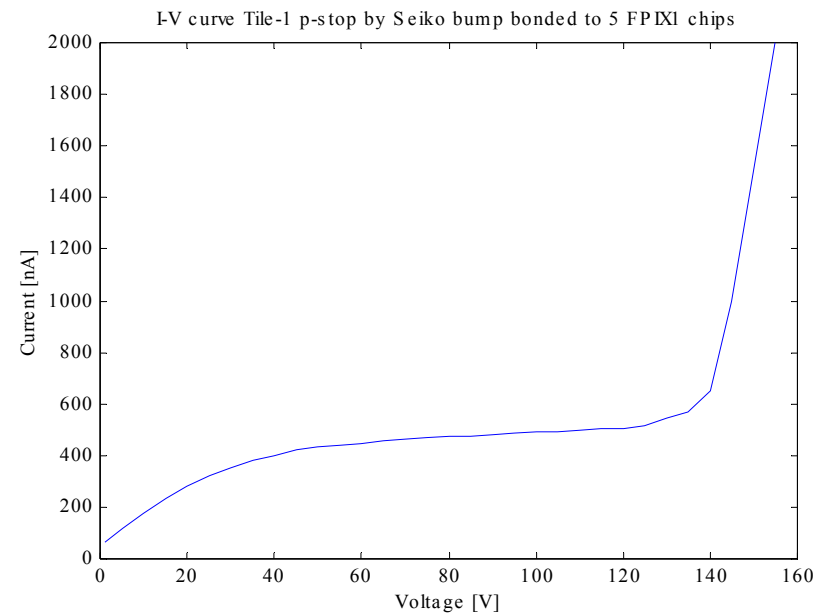
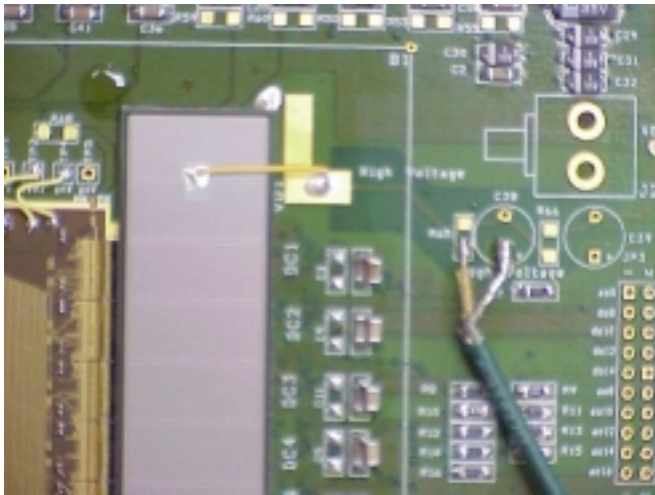
Atlas Pixel prototypes made by Seiko - Tile-1, atoll p-stop



High Voltage bias using wire bond. Not a good idea...



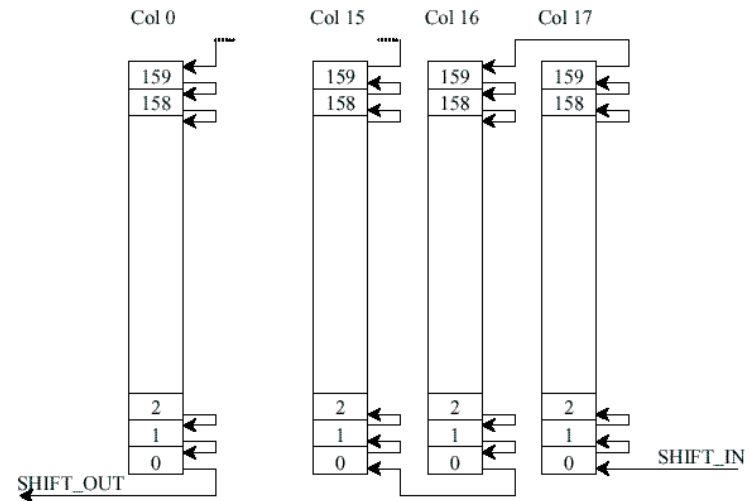
...replaced by wire with plastic isolator.



First Results : 4 chips working!

What happened with 5th one?

Initialization shift register not working for kill/inject pattern. All other shift registers working perfectly. It means that the initialization of this shift register wasn't going through this chip. This is done in a daisy chain fashion, so the chips after this weren't being initialized.



First Results: Calibration

$V_{th0}=1.95V$	Threshold [e]		Noise [e]	
	μ	σ	μ	σ
Chip 0	6212.43	349.375	70.63	17.15
Chip 1	5842.2	391.65	68.67	43.2
Chip 2	6101.5	271.1	60.52	25.78
Chip 3	7277.50	323.36	75.51	47.25